



AD04VI - Setra 0~5VDC output Wiring						DATE	2008.08.01	TITLE	Analog input
						DESIGN	mst	DWG. NO	01
						CHECK	mst	PROJECT	MST-2008-01
						APPROVE	mst		
DATE	ZONE	REV CONTENTS	REV CAUSE	REV NO	APPROVED				
REVISION STATUS									

MST SYSTEM
Micro Solution Technology